

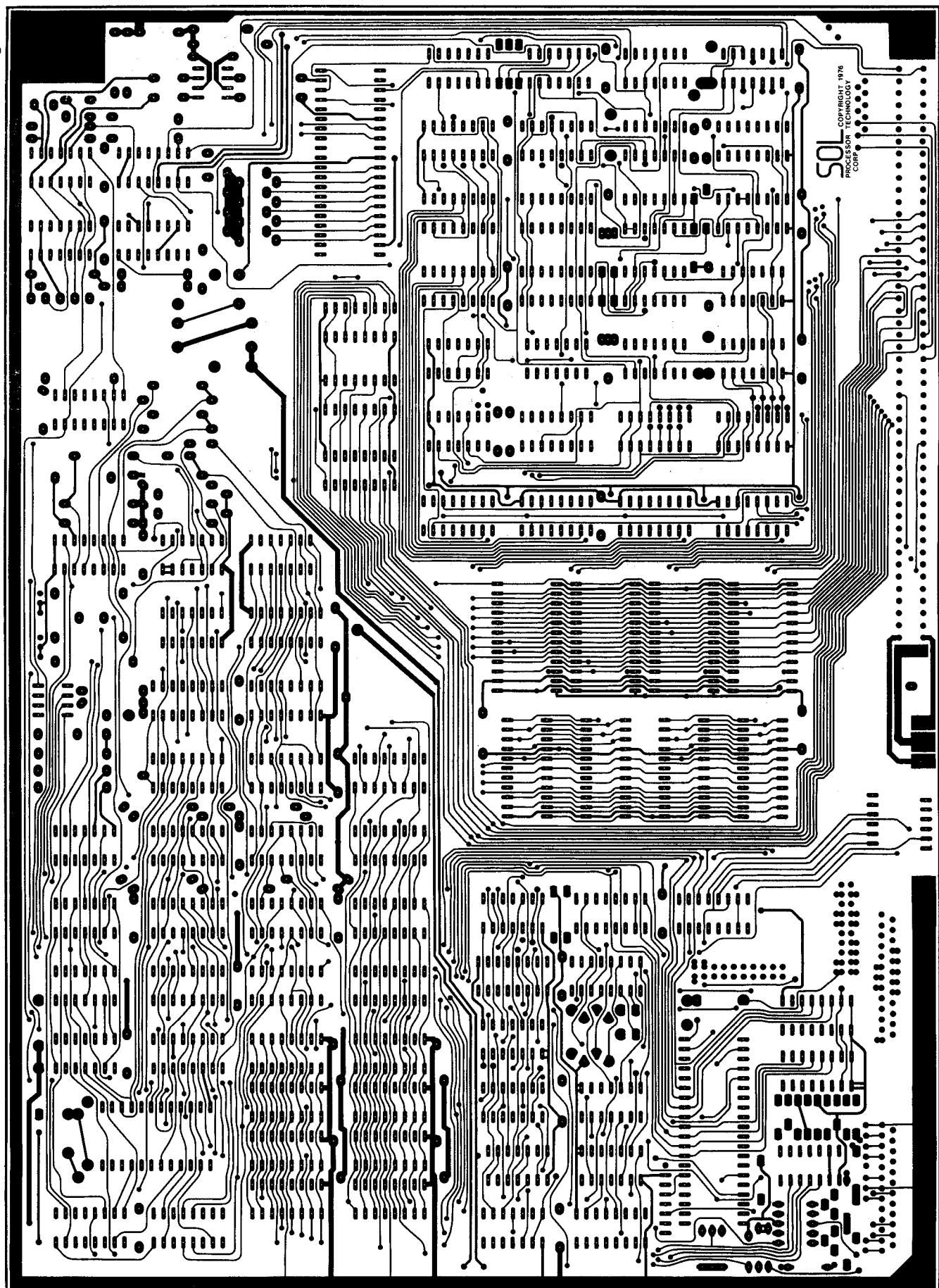


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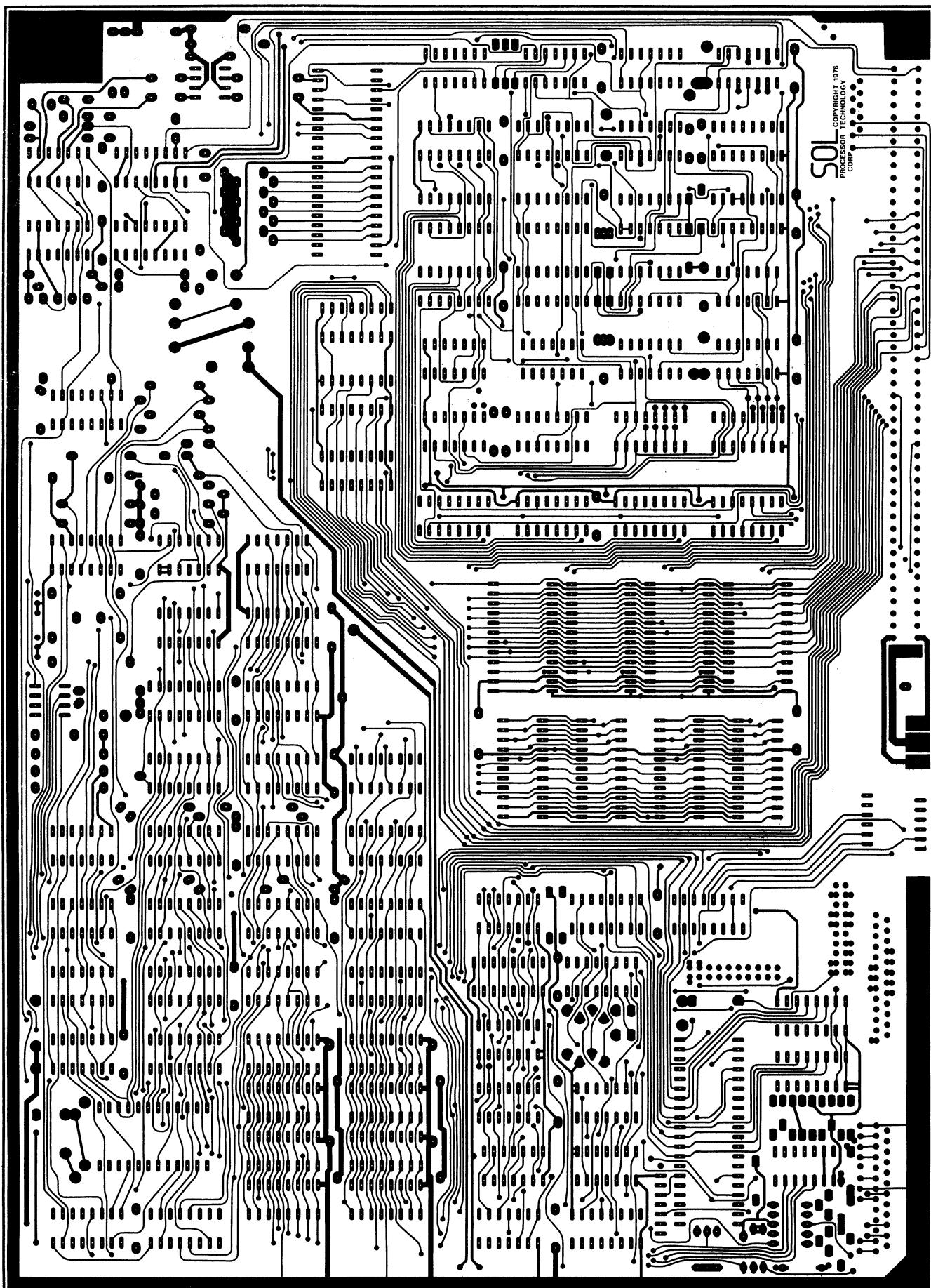
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TOP



ALL C'NTL CIR

ALL C'NTL CIR

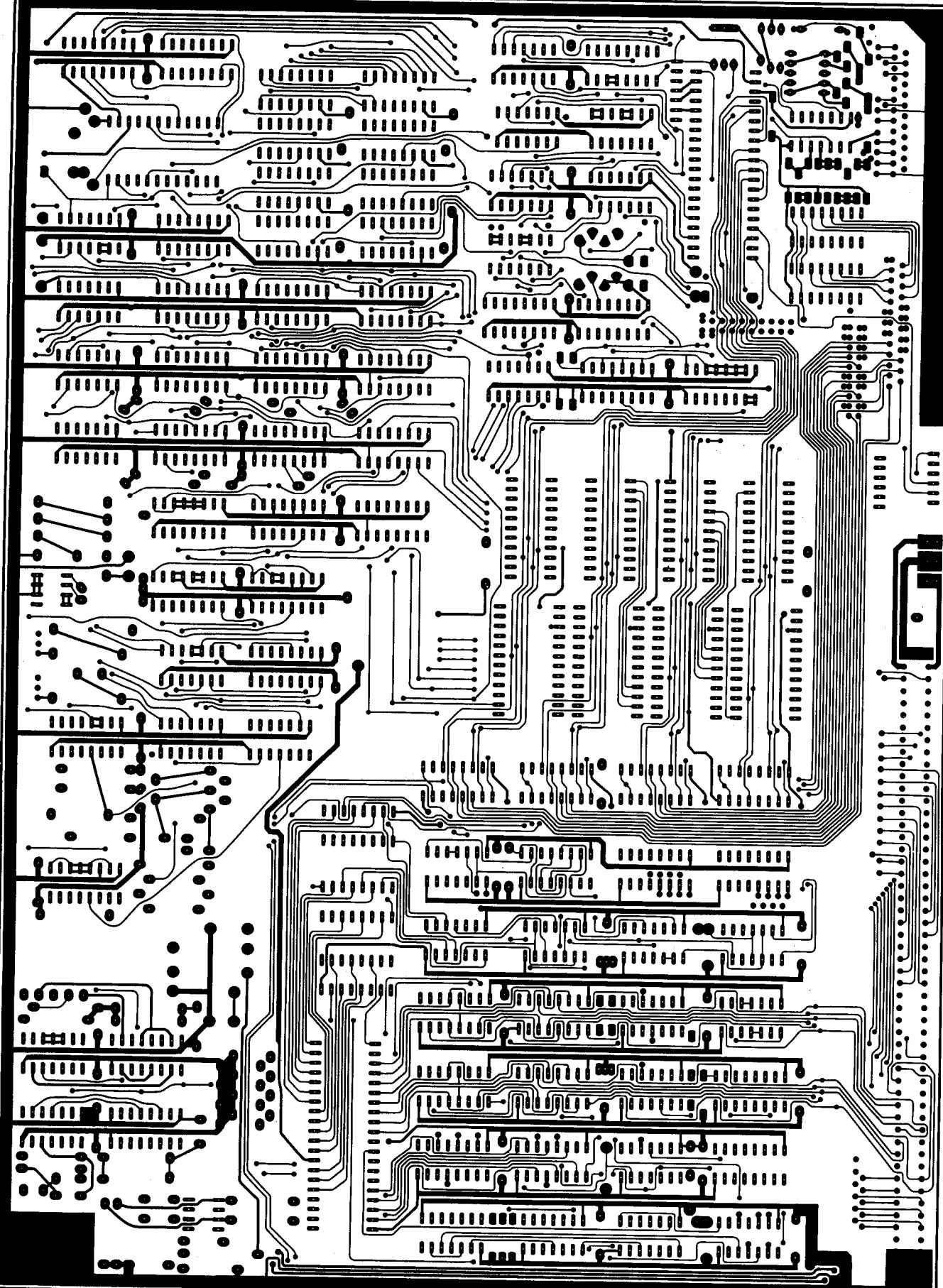


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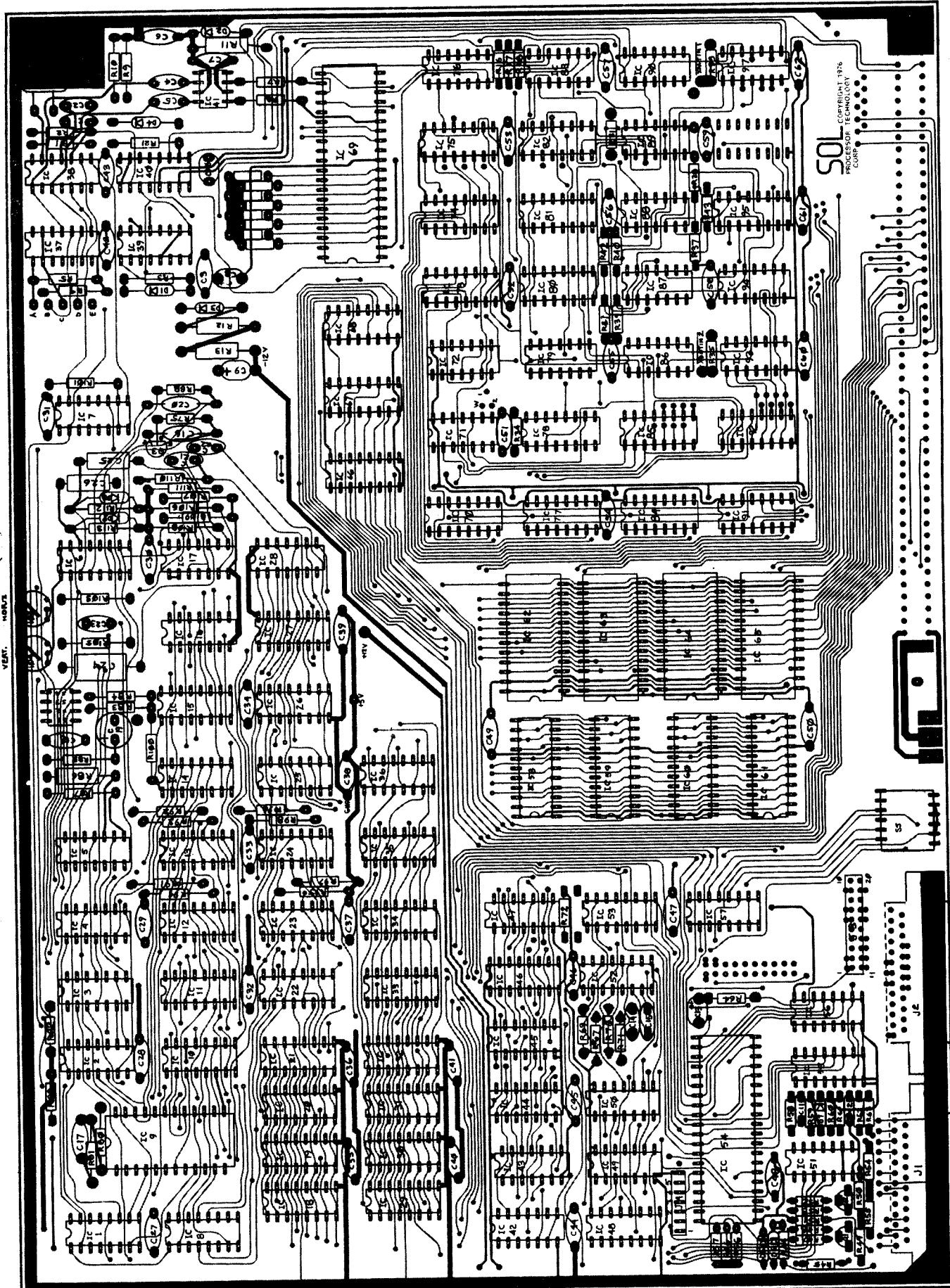
9.50

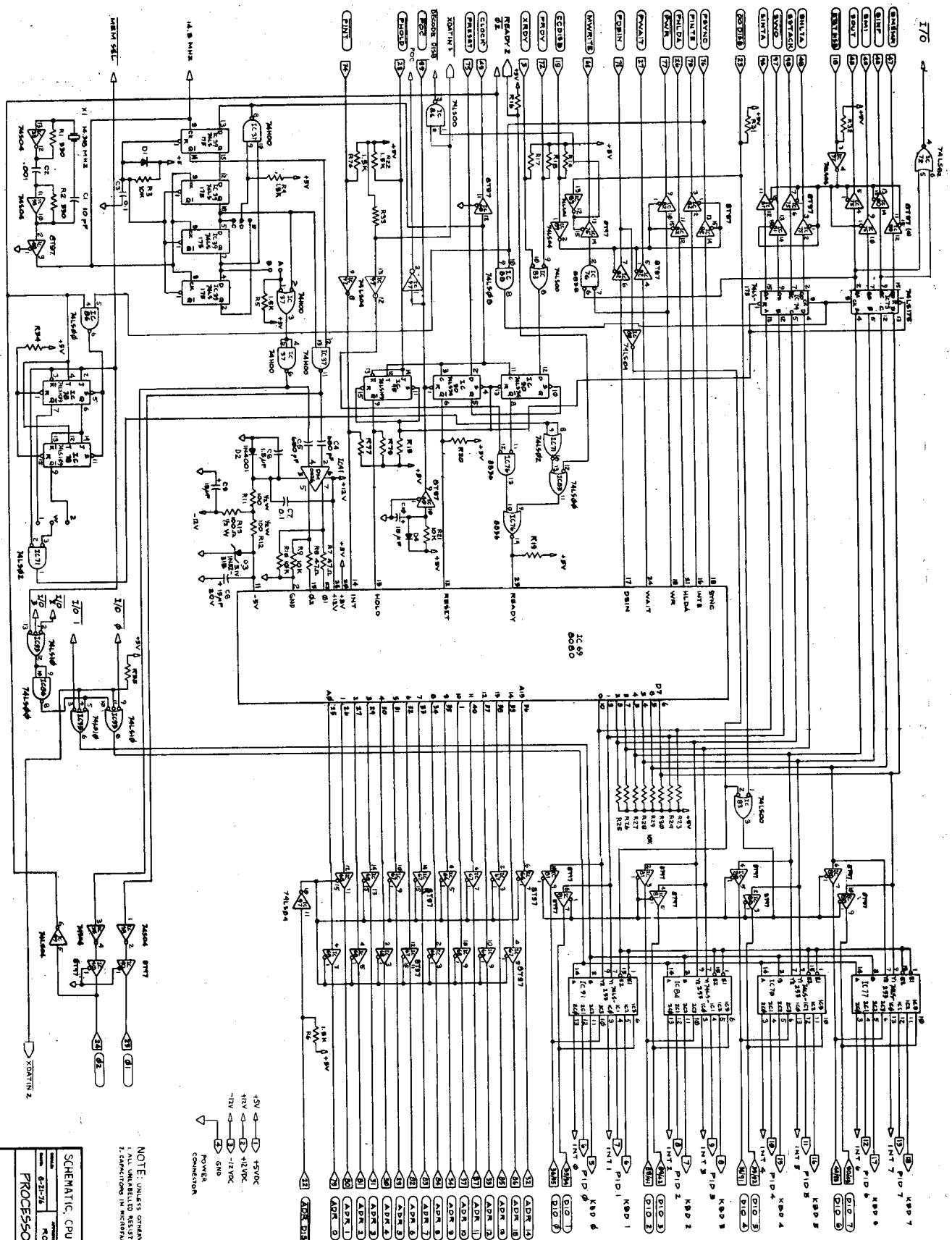
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BOTTOM



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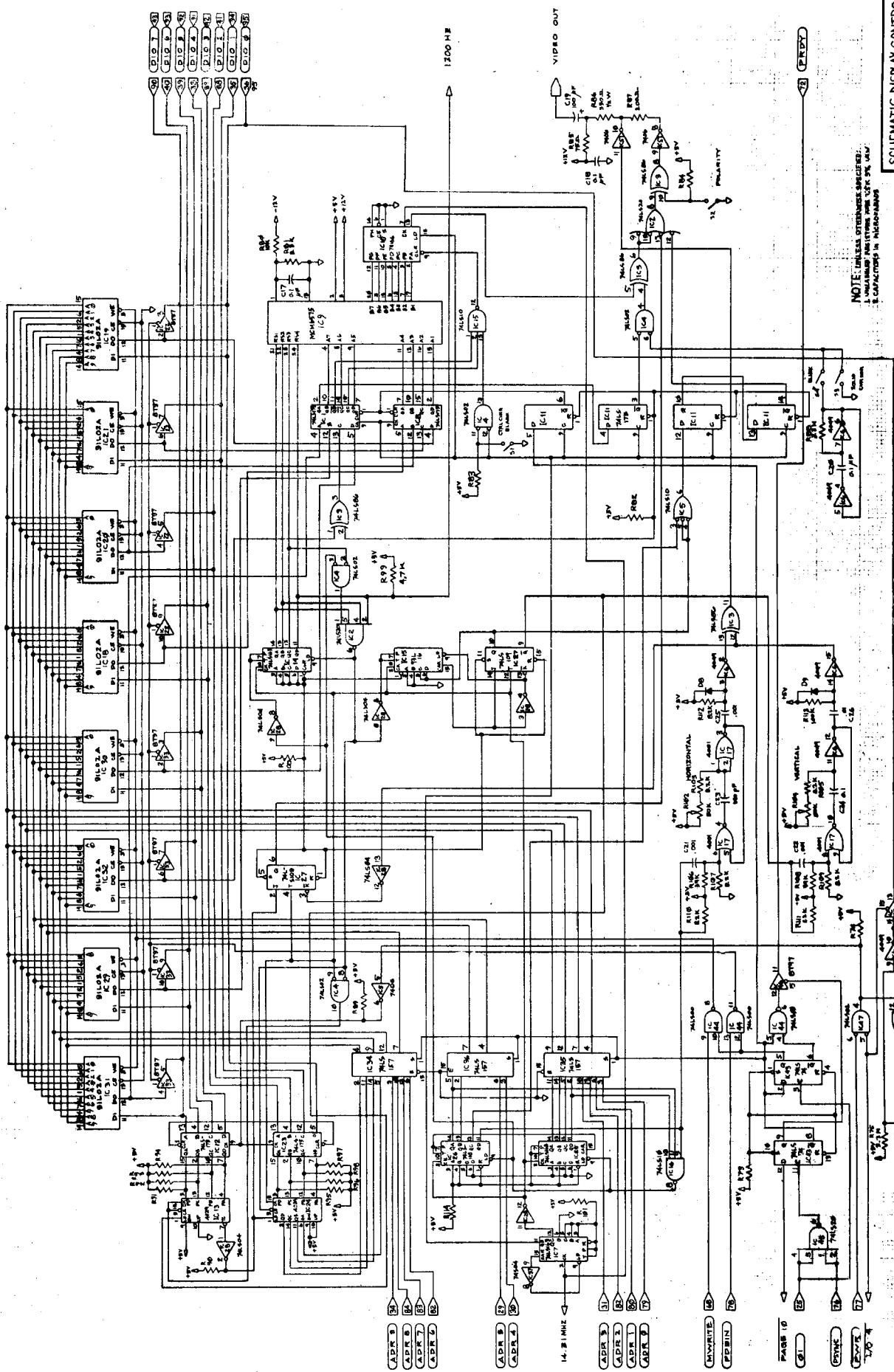
NOTE: UNLESS OTHERWISE SPECIFIED:
1. ALL UNLABELLED RESISTORS 1.5 KΩ AND 5
2. CAPACITORS IN MICROFARADS

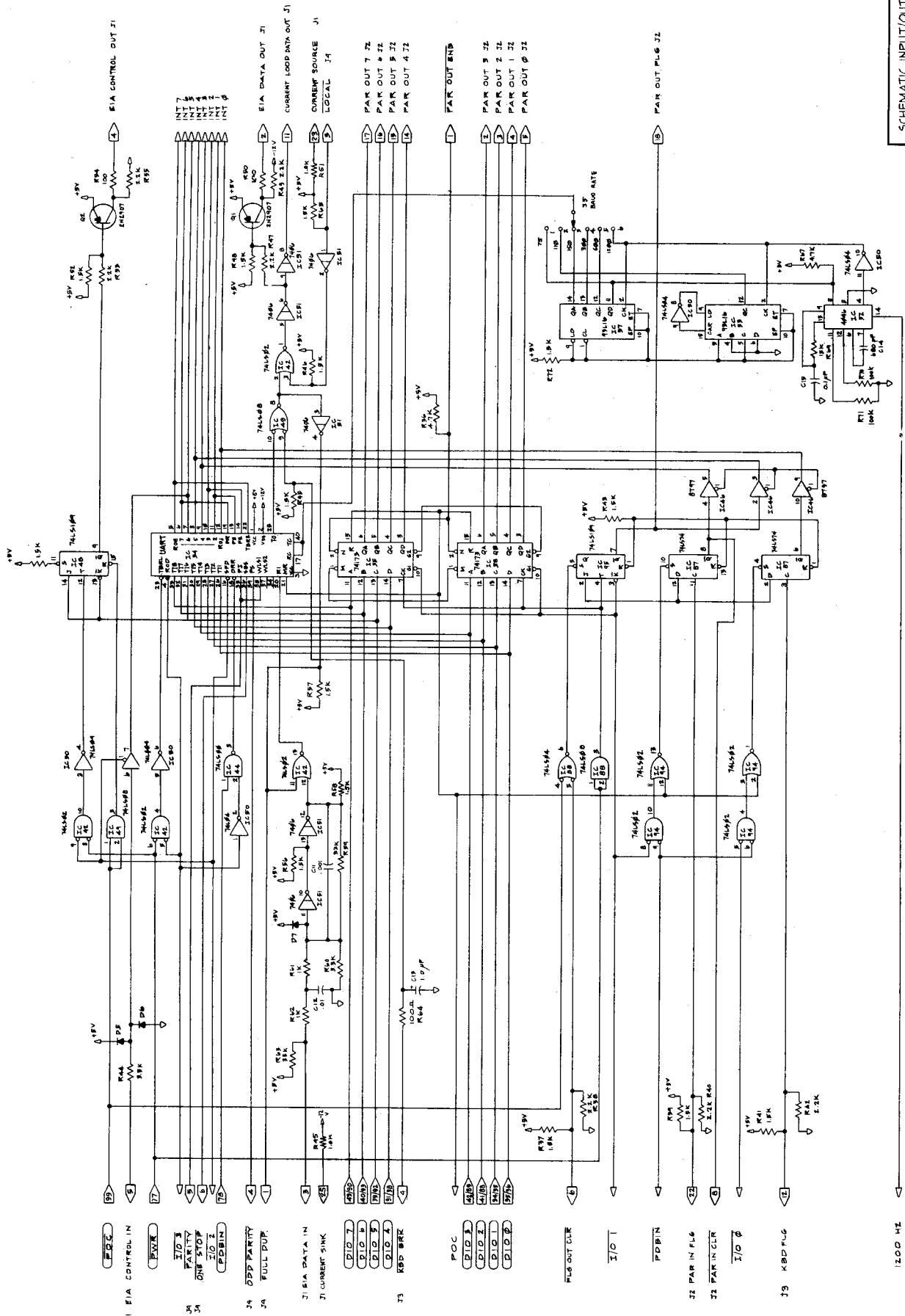
SCHEMATIC, CPU AND BUS, Sol -I

NOTE: UNLESS OTHERWISE SPECIFIED:
1. ALL UNLABELLED RESISTORS 1.5 KΩ AND 5%
2. CAPACITORS IN MICROFARADS

NOTE: UNLESS OTHERWISE SPECIFIED:
1. ALL UNLABELLED RESISTORS 1.5 KΩ AND 5%
2. CAPACITORS IN MICROFARADS

NOTE: UNLESS OTHERWISE SPECIFIED:
1. UNLAWFUL PRACTICES ARE NOT TO BE USED
2. CAPACITORS IN MICROGRAMS

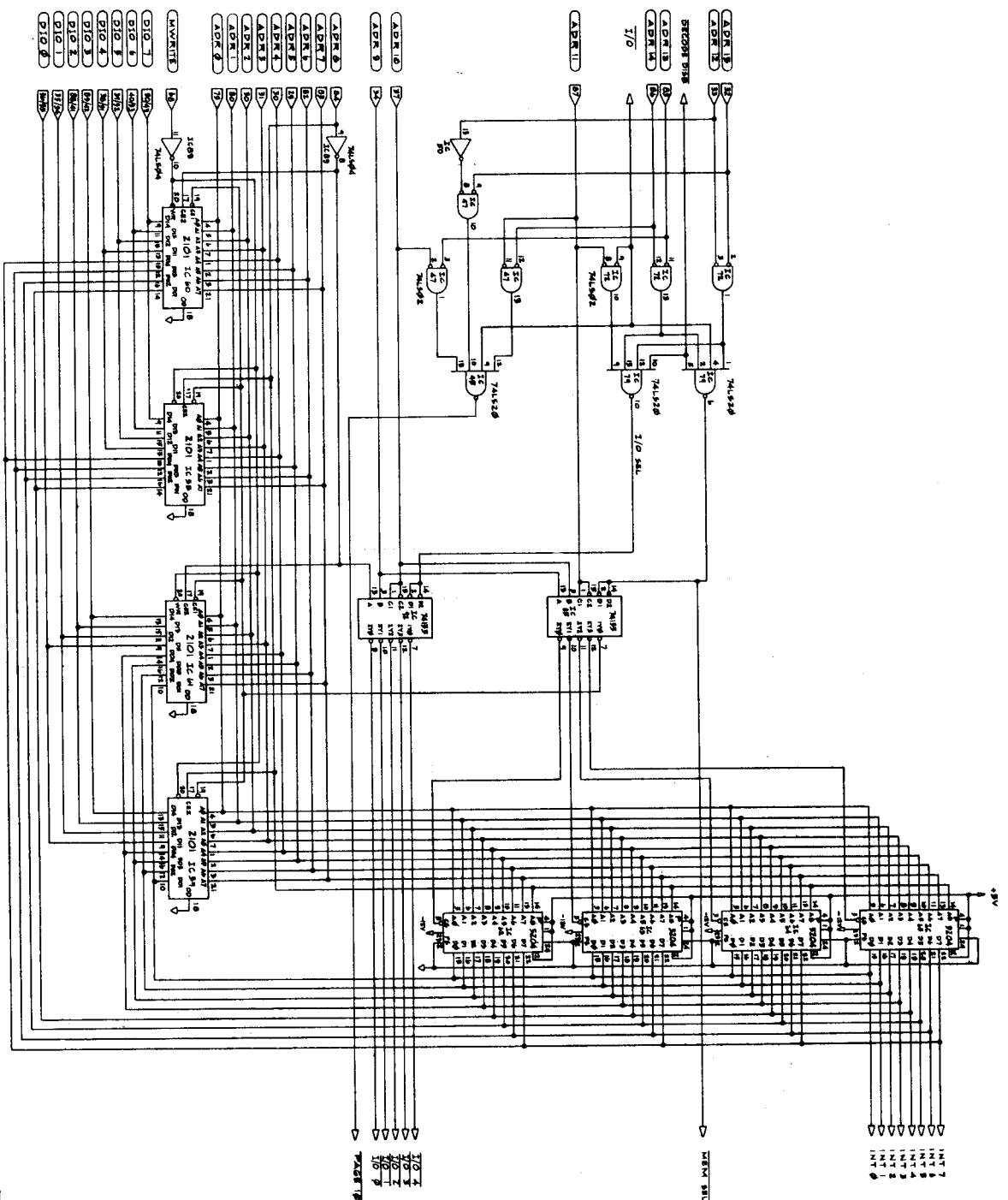




SCHEMATIC, INPUT/OUTPUT, SOL-I

SHEET NO.	6-24-78	DRAWN BY	ROBERT M. MASSH	REVIEWED BY	LITO
DESIGN		INSTRUMENTATION		TESTING	

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SCHEMATIC, MEMORY, S01-I

ROBERT M. MARSH

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